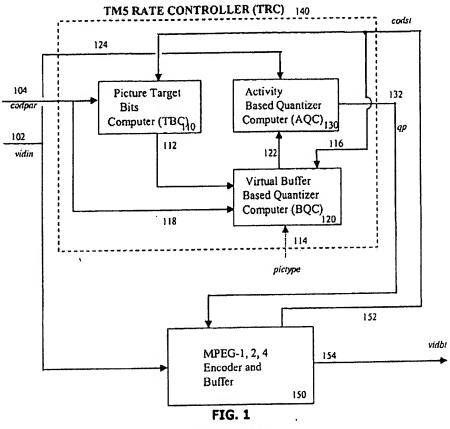
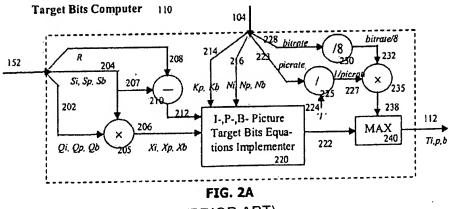
Application No. 10/811,960; Filed 03/30/2004
RATE CONTROL FOR VIDEO CODER EMPLOYING
ADAPTIVE LINEAR REGRESSION BITS MODELING
Inventor: Atul Puri; Attorney Docket No. 13316/3294
REPLACEMENT SHEET 1-OF 28

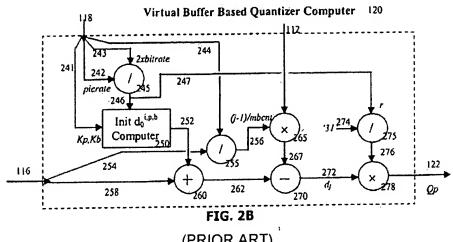
MPEG ENCODER WITH TM5 RATE CONTROLLER 100



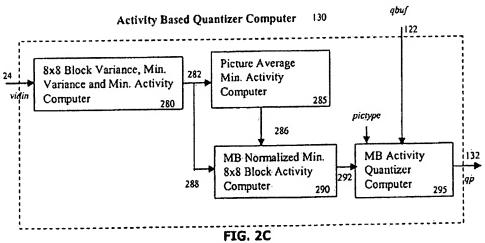
(PRIOR ART)



(PRIOR ART)







(PRIOR ART)

H.264/MPEG-4 AVC ENCODER WITH RQC RATE CONTROLLER Video Content Rate and Quality vidia zi fldec Adaptive Controller 301 Preprocessor 318 (RQC) 307 VLC/CAVLC/ Forward Quantizer, 4x4 Forward Buſ 316 311 CABAC Encoder and Transform Scaler, Integer Approx. fer₃₉₅ Bitstream Formatter DCT-like Select Coef Zeroer, and Forward Scan Transform Ī 389 ptyp,styp mbtyp,cbp,dqp,ref,dmv ,dbflpar Inverse Scan, Transform Scaler, and Inverse Quantizer, 328 modedeci (lambda) 4x4 Inverse Integer Approx. DCT-like Transform 332 +)335 Rowsize+1 336 16x16 and 4x4 MBs Store 348 Intra Predictor Deblocking 16x16 and 4x4 Sel 377 Filter dbflfpar (alpha_c0_ Intra/MB Partioffset, beta_offset) ioned MC Inter **MB** Partitions Mode Decider Motion Compensated (MC) Wei-Multiple Past / 573 371 352 ghted Predictor Future Reference Pictures Store 368 MV Scaler

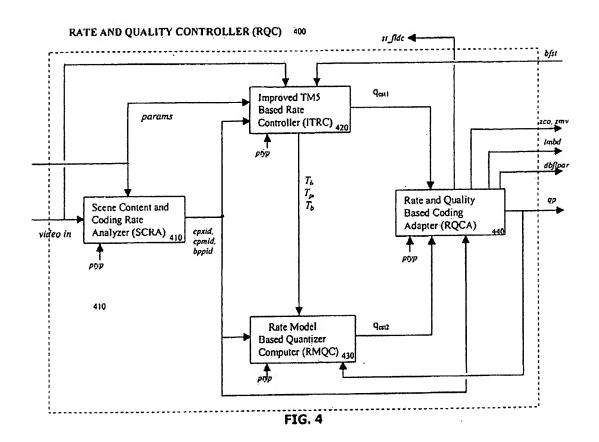
FIG. 3

or Zeroer

MB Partitions Multi-Reference

Motion Estimator 360

309



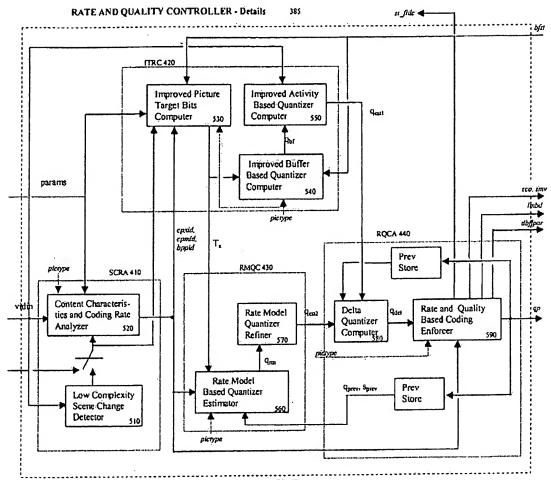
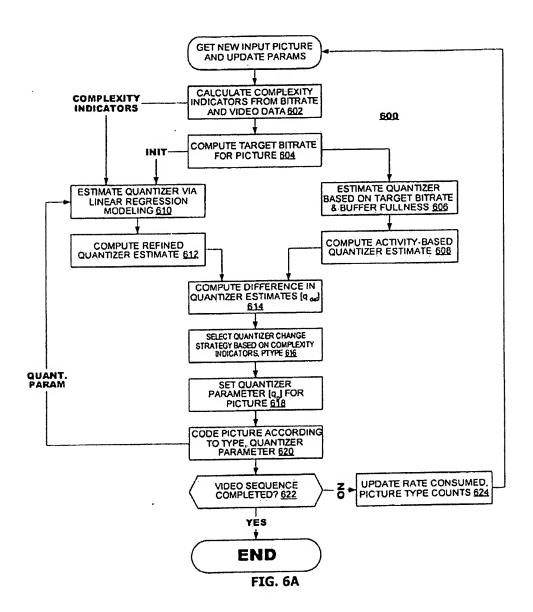
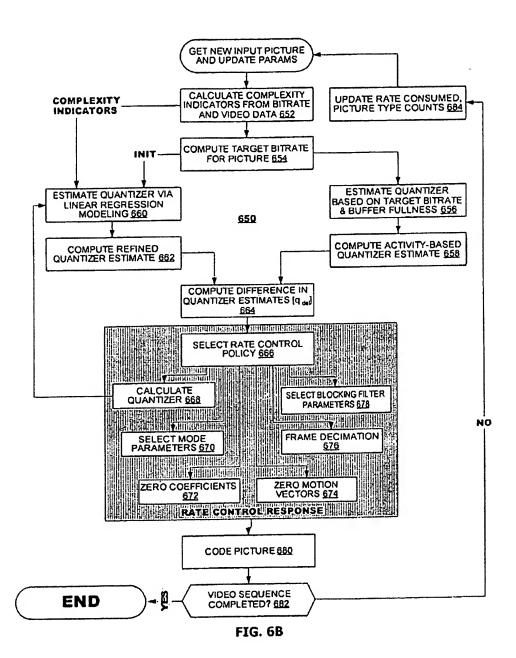


FIG. 5





700- Video frames coding order when employing 2 B-frame coding structure

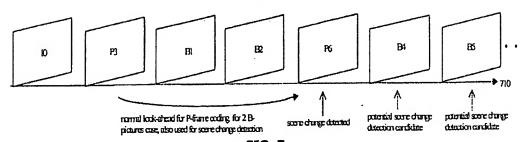


FIG. 7

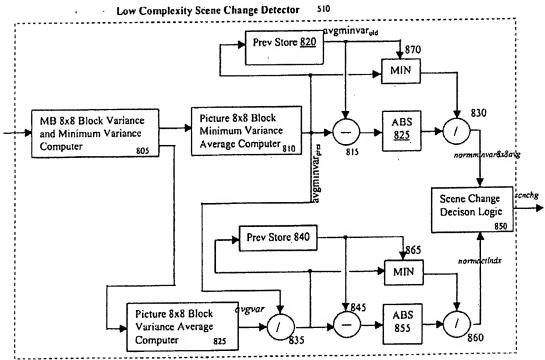
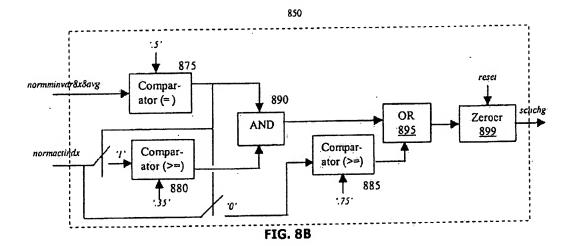
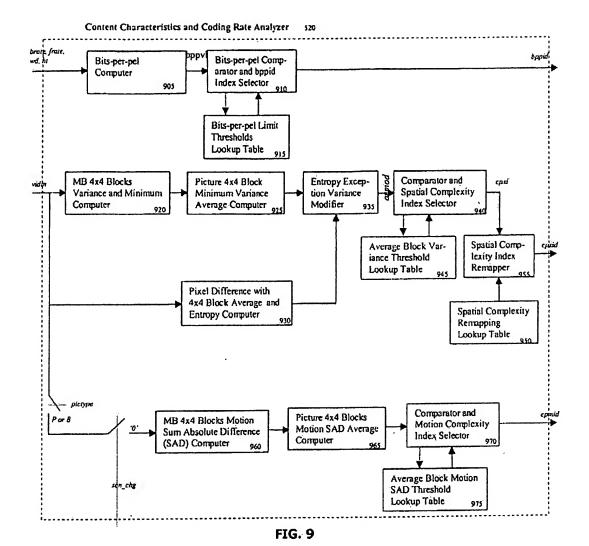


FIG. 8A





brate | bppvl | frate | wd | ×

FIG. 10A

hi

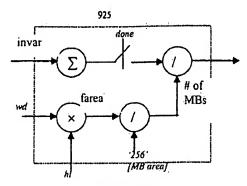


FIG. 10B

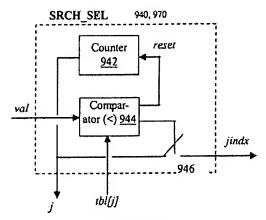


FIG. 10C

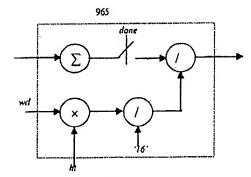


FIG. 10D

Application No. 10/811,960; Filed 03/30/2004 RATE CONTROL FOR VIDEO CODER EMPLOYING ADAPTIVE LINEAR REGRESSION BITS MODELING Inventor: Atul Puri; Attorney Docket No. 13316/3294 REPLACEMENT SHEET 12 OF 28

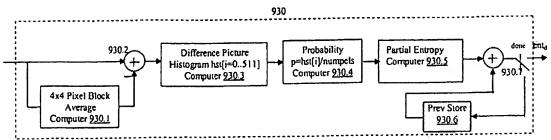
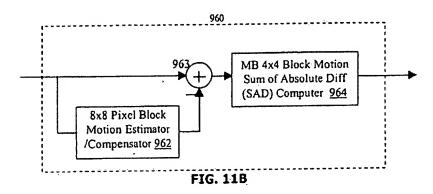


FIG. 11A



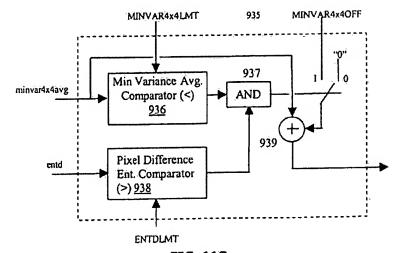


FIG. 11C

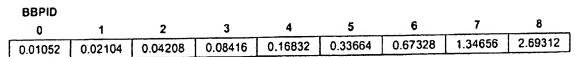
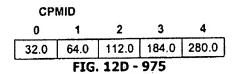


FIG. 12A - 915

Application No. 10/811,960; Filed 03/30/2004 RATE CONTROL FOR VIDEO CODER EMPLOYING ADAPTIVE LINEAR REGRESSION BITS MODELING Inventor: Atul Puri; Attorney Docket No. 13316/3294 REPLACEMENT SHEET 13 OF 28

			71.0	84.0	100.0 FIG. 12	L		177.0			
			12	13	14	15	16	17			
1.0	2.0	4.5	8.5	12.5	17.0	22.0	28.0	34.0	41.0	50.0	60.0
0	1	2	3	4	5	6	7	8	9	10	11
CPID											

CP:	XID										
0	1	2	3	4	5	6	7	8			
1	3	5	7	9	11	13	15	17			
·	FIG. 12C -955										



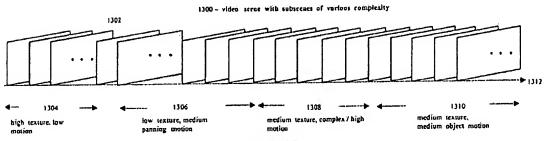
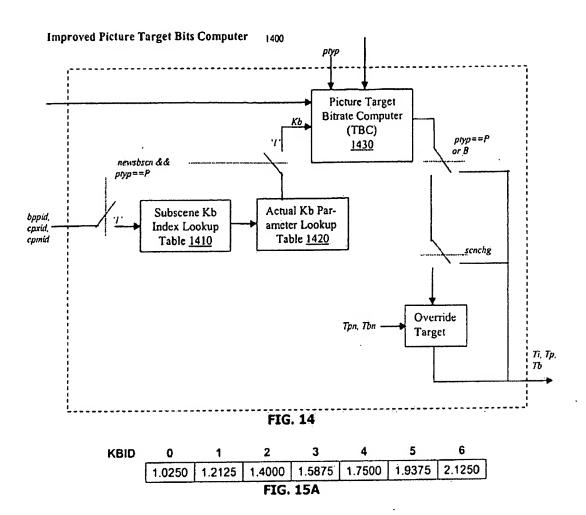


FIG. 13

Application No. 10/811,960; Filed 03/30/2004 RATE CONTROL FOR VIDEO CODER EMPLOYING ADAPTIVE LINEAR REGRESSION BITS MODELING Inventor: Atul Puri; Attorney Docket No. 13316/3294 REPLACEMENT SHEET 14 OF 28



					C	PXI	D				CPMID=1				C	PXI	D			
CPMID)=2	Ó	1	2	3	4	5	6	7	8	CP#IID~1	0	1	2	3	4	5	6	7	8
	0	2	2	2	3	3	3	4	4	4	0	2	2	2	2	2	3	3	3	3
	1	2	2	2	2	3	3	3	4	4	1	2	2	2	2	2	2	3	3	3
	2	2	2	2	2	2	3	3	3	4	2	2	2	2	2	2	2	2	3	3
Ω	3	2	2	2	2	2	2	3	3	3	3 ۾	2	2	2	2	2	2	2	2	3
BPPID	4	2	2	2	2	2	2	2	3	3	Qldd8	2	2	2	2	2	2	2	2	2
<u> </u>	5	2	2	2	2	2	2	2	2	3	ω ₅	1	2	2	2	2	2	2	2	2
	6	1	2	2	2	2	2	2	2	2	6	1	1	2	2	2	2	2	2	2
	7	1	1	2	2	2	2	2	2	2	7	1	1	1	2	2	2	2	2	2
	8	1	1	1	2	2	2	2	2	2	8	1	1	1	1	2	2	2	2	2
										FIG	15B									

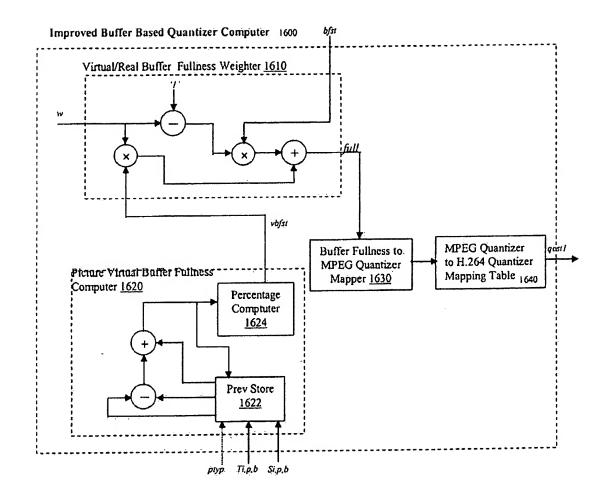


FIG. 16

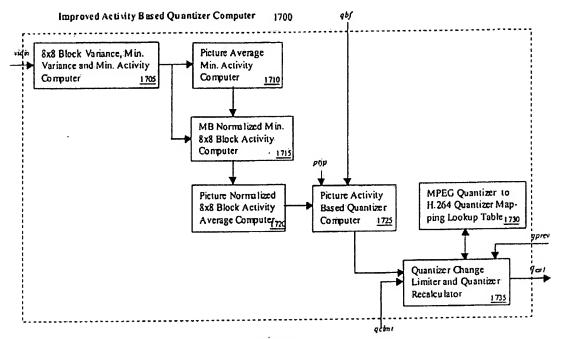


FIG. 17

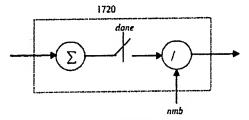
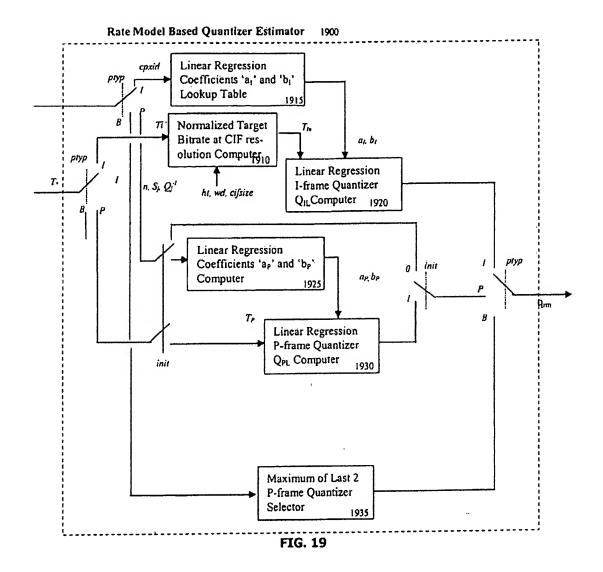


FIG. 18A

Application No. 10/811,960; Filed 03/30/2004 RATE CONTROL FOR VIDEO CODER EMPLOYING ADAPTIVE LINEAR REGRESSION BITS MODELING Inventor: Atul Puri; Attorney Docket No. 13316/3294 REPLACEMENT SHEET 17 OF 28

qh:	264											
0	1	2	3	4	5	6	7	8	9	10	11	12
.250	.281	.315	.353	.396	.446	.500	.561	.623	.707	.794	.891	1.00
13	14	15	16	17	1,8	19	20	21	22	23	24	25
1.12	1.26	1.41	1.59	1.78	2.00	2.25	2.52	2.82	3.18	3.56	4.00	4.49
26	27	28	29	30	31	32	33	34	35	36	37	38
5.04	5.65	6.35	7.13	8.00	8.98	10.08	11.31	12.70	14.25	16.00	17.96	20.16
39	40	41	42	43	44	45	46	47	48	49	50	51
22.63	25.39	28.51	32.00	35.92	40.31	45.25	50.80	57.02	64.00	71.83	80.64	90.51
	I <u>.,,</u>	L	·	·		FIG. 1	BB					

1800 qdiflmt nlmqnt[qoff+j]qoff Prev Store pictyp = B Limiter ABS Comparator [0,30] Mq SRCH_ (>) 1812 1810 1824 SEL 1802 1804 qindx qindx +qoff qindx+qoff - qprev qindx 1816 1814 qprev - qoff FIG. 18C



CPID					
0	1	2	3	4	5
-68134.59213	-87003.98467	-106202.60465	-125401.23463	-133506.23620	-141558.73699
6	7	8	9	10	11
-149611.24778	-151588.19751	-220858.39744	-293963.81117	-254808.46319	-215653.11522
12	13	14	15	16	17
-207487.50918	-1993321.90315	-191155.48428	-182989.06541	-178235.75132	-169521.36854

FIG. 20A

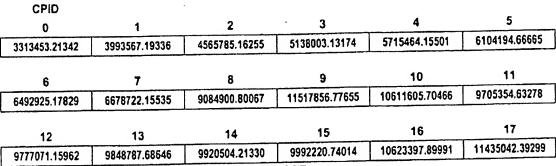
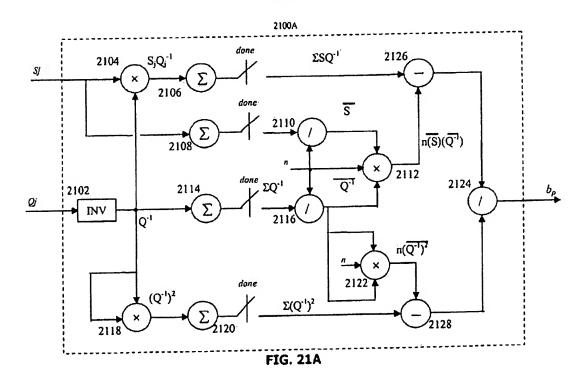


FIG. 20B



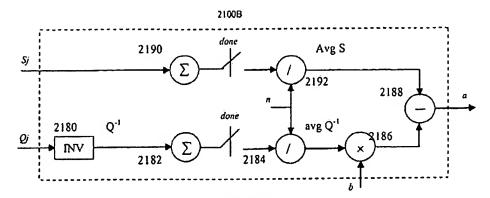


FIG. 21B

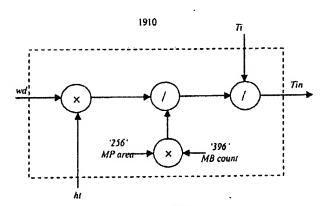
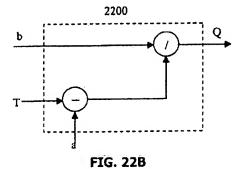
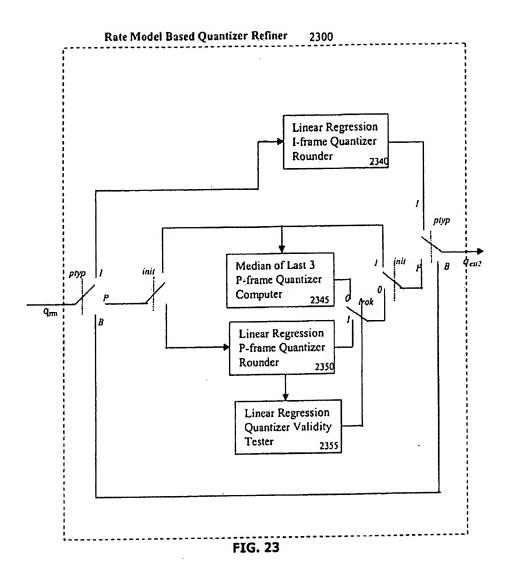
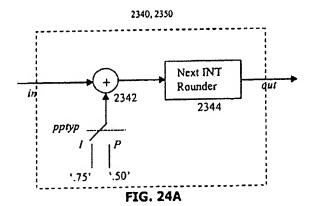


FIG. 22A







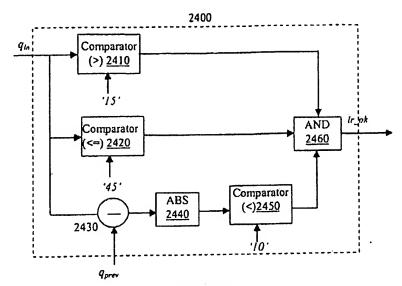


FIG. 24B

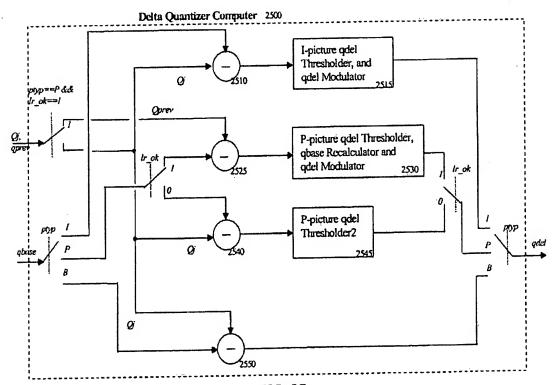
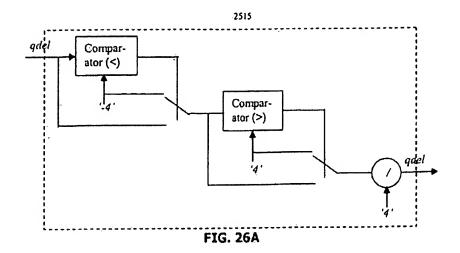
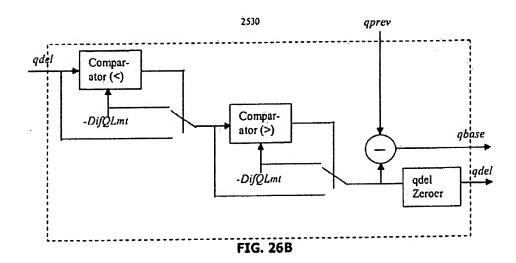
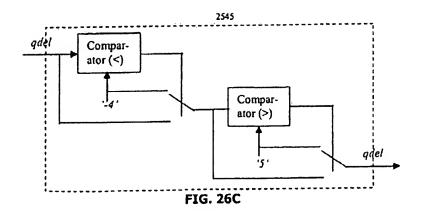


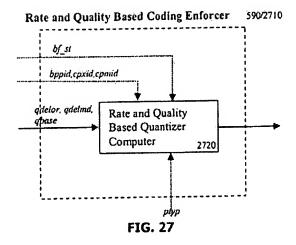
FIG. 25



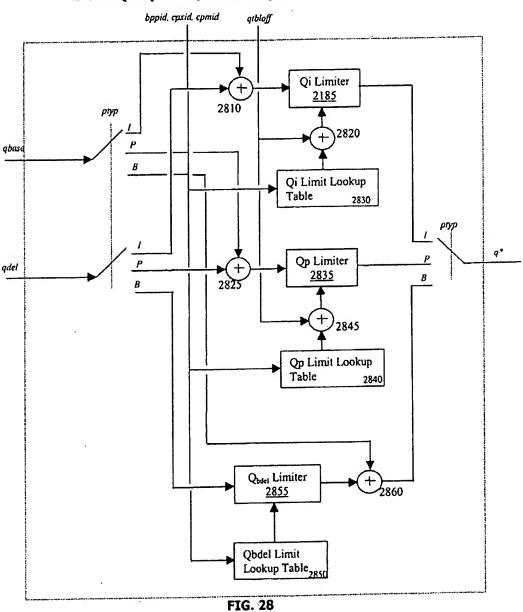




Application No. 10/811,960; Filed 03/30/2004
RATE CONTROL FOR VIDEO CODER EMPLOYING
ADAPTIVE LINEAR REGRESSION BITS MODELING
Inventor: Atul Puri; Attorney Docket No. 13316/3294
REPLACEMENT SHEET 24 OF 28







Application No. 10/811,960; Filed 03/30/2004 RATE CONTROL FOR VIDEO CODER EMPLOYING ADAPTIVE LINEAR REGRESSION BITS MODELING Inventor: Atul Puri; Attorney Docket No. 13316/3294 REPLACEMENT SHEET 26 OF 28

						CPXID				
		0	1	2	3	4	5	6	7	8
	0	10	11	12	13	14	15	16	17	18
	1	9	10	11	12	13	14	15	16	17
	2	8	9	10	11	12	13	14	15	16
۵	3	7	8	9	10	11	12	13	14	15
BPPID	4	6	7	-8	9	10	11	12	13	14
<u> </u>	5	5	6	7	8	9	10	11	12	13
	6	4	5	6	7	8	9	10	11	12
	7	3	4	5	6	7	8	9	10	11
	8	2	3	4	5	6	7	8	9	10
	,	•			FIG. 2	9A				

						CPXID				
		0	1	2	3	4	5	6	7	8
	0	10	11	12	13	14	15	16	17	18
	1	9	10	11	12	13	14	15	16	17
	2	8	9	10	11	12	13	14	15	16
3	3	7	8	9	10	11	12	13	14	15
2	4	6	7	8	9	10	11	12	13	14
ā	5	5	6	7	8	9	10	11	12	13
	6	4	5	6	7	8	9	10	11	12
	7	3	4	-5	6	7	8	9	10	11
		2	2	1	6	6	7	R	9	10

FIG. 29B

						CPXID				
		0	1	2	3	4	5	6	7	8
	0	2	2	2	3	3	3	4	4	4
	1	1	2	2	2	3	3	3	4	4
	2	1	1	2	2	2	3	3	3	4
0	3	1	1	1	2	2	2	3	3	3
BPPID	4	1	1	1	1	2	2	2	3	3
∞	5	0	1	1	1	2	2	2	2	3
	6	0	0	1	1	1	2	2	2	2
	7	0	0	0	1	1	1	2	2	2
	8	0	0	0	0	1	1	1	2	2

FİG. 29C

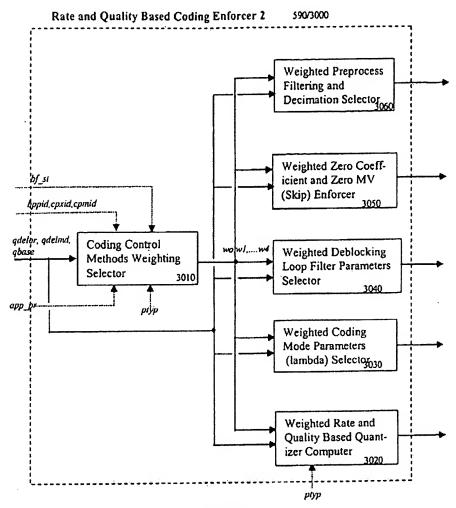


FIG. 30

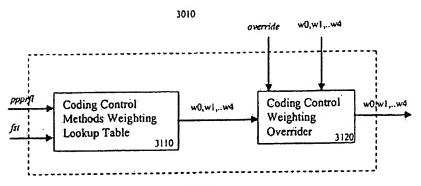


FIG. 31

					4	1						
					3	0.37	0.05	0.16	0.01	0.28	0.06	0.19
			- 2	2 [0.09	0.12	0.46	0.26	0.21	0.15	0.07	0.36
			1 [0.22	0.45	0.16	020	024	0.45	0.18	0.09	0.38
		0	0.09	0.09	0.05	0.27	0.21	0.27	0.31	0.25	0.09	0.33
арр	_pr	Ō.	1	2	3	4	5	6	0.13	0.26	0.08	0.39
	ď	023	0.30	0.18	0.26	0.07	0.07	0.26	0.14	0.23	0.1	0.3
	1	0.11	0.48	0.25	0.03	0.23	0.15	0.19	0.12	0.27	0.08	0.34
	2	0.08	0.21	0.45	0.18	0.15	0.12	0.15	0.14	021	0.08	0.35
	3	0.44	0.39	0.07	0.09	0.31	0.45	0.28	0.11	0.23	0.09	0.29
=	4	0.47	0.30	0.28	0.12	0.08	0.35	0.10	0.12	0.24	0.07	0.32
ptst	5	0.44	0.37	0.10	0.30	0.29	0.15	0.41	0.13	0.2	0.08	
	6	020	0.47	0.38	0.22	0.44	0.27	0.27	0.1	0.22		
	7	0.10	0.12	0.47	0.27	0.10	0.09	022	0.11			
	8	0.30	0.22	0.49	0.46	0.18	0.49	0.47				
	9	0.11	0.44	0.07	0.03	0.36	0.09	0.35				

FIG. 32

Weighted Rate and Quality Based Quantizer Computer 3020

